### The Dielectric-Filled Parabola: Concept and Applications Review

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The dielectric-filled parabola [1] is a thick-substrate-lens-antenna implementation based on the pioneering work of Rutledge et. al. [e.g. 2]. It was conceived [3] as a simple means of creating a large f-number beam from a photolithographically fabricated antenna or modest planar array. In implementation, it consists of a plano-convex dielectric lens whose curved surface takes the shape of a parabola ( $y^2=4fx$ ) and whose flat surface resides at the focal point of the lens. In operation, the parabolic surface is metallized and the receiving or transmitting antenna and associated solid-state driving circuitry are mounted on a separate dielectric wafer which lies in intimate contact with the flat surface. Illumination takes place from the front (flat side) and, as in the case of [4], through a small hole left in the metal at the center of the curved surface. The structure has several features which make it advantageous for use at millimeter and submillimeter wavelengths including inherently high f-number, simple-to-apply matching layers, reasonable dielectric path length, reduced off-axis aberration (compared to an unfilled parabolic mirror) etc.

Since its original conception, the dielectric-filled parabola has been used successfully to demonstrate the first millimeter-wave superconducting planar heterodyne array [5], an all-planar millimeter/submillimeter-wave fundamental Schottky diode mixer [6] and recently a novel quad-bridge multiplier concept [7]. These applications will be reviewed and several interesting design improvements/modifications will be considered.

[1]. P.H. Siegel and R.J. Dengler, "The Dielectric-Filled Parabola: A New Millimeter/Submillimeter Wavelength Receiver/Transmitter Front End," *IEEE Trans. Antennas and Propagation*, vol. 39, no. 1, pp. 40-48, Jan. 1991.

[2]. D.B. Rutledge, D.P. Neikirk and D.P. Kasilingam, "Integrated Circuit Antennas," in *Infrared and Millimeter Waves*, vol. 10., New York: Academic Press, 1983, pp. 1-90.

[3]. P.H. Siegel, "Dielectric Filled Parabola with Integrated Planar Antenna for use as a Millimeter/Submillimeter Wavelength Receiver/Transmitter Front End," JPL New Technology Report 17802/7300, Feb. 1989.

[4]. P.H. Siegel, "A Submillimeter-Wave Heterodyne Array Receiver Using a Dielectric-Filled Parabola: Concept and Design," *1st International Symposium on Space THz Technology*, pp. 218-234, Mar. 5-6, 1990

[5]. P.A. Stimson, R.J. Dengler, H.G. LeDuc and P.H. Siegel, "Superconducting Heterodyne Planar Array Mixer Using a Dielectric-Filled Parabola: Status and Measured Performance," *Multi-Feed Systems for Radio Telescopes*, Tucson, AZ, May 16-17, 1994

[6]. P.H. Siegel, "An Open-Structure Mixer Using an All-GaAs Integrated Diode/Antenna Wafer for Millimeter and Submillimeter Wavelengths," JPL New Technology Report, NPO-19371/8972, January, 1994.

[7]. M. Kim, J. Bruston, R.P. Smith, S.C. Martin and P.H. Siegel, "A Slot-Antenna Bridge-Frequency Multiplier Design Suitable for Submillimeter Wavelengths," *Submitted to 1996 IEEE AP-S International Symposium on Antennas and Propagation*, January 1996.

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Seventh International Symposium on Space THz Technology March 12-14, 1996

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## Talk Outline

- 1. Dielectric-Filled Parabola: Concept
- 2. Dielectric-Filled Parabola: Properties
- 3. Dielectric-Filled Parabola: Applications
- a. Single Element Planar Antenna Receiver/Transmitter
- b. Multibeam Superconducting Planar Array
- c. Ultra-High Frequency Semiconductor-Diode Mixer
- d. Planar Quad-Bridge Varactor-Diode Multiplier



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Dielectric-Filled Parabola: Advantages     Onceived as a notential solution to a variety of high freq. planar antenna problems	<ul> <li>Combines the thick substrate lens concept of Rutledge et. al. with the high directivity inherent in parabolic reflectors</li> </ul>	<ul> <li>Converts low-directivity half-wave resonant planar antennas to high f-number output beam with a single low-loss low-dielectric-constant lens</li> </ul>	<ul> <li>Maintains a flat input/output plane for simple incorporation of substrate wafers, matching layers, or corrective optics</li> </ul>	<ul> <li>Relatively insensitive to lateral and axial defocusing</li> </ul>	<ul> <li>Modest beam steering capability with predictable coma lobe for off-axis elements</li> </ul>	<ul> <li>Slightly shorter dielectric path length than hyperhemisphere or elliptical lenses</li> </ul>	<ul> <li>Cassegrain style LO injection capability</li> </ul>	<ul> <li>Suitable for room temperature or cryogenic operation</li> </ul>
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Seventh International Symposium on Space Terahertz Technology, Charlottesville, March 1996 Dielectric-Filled Parabola: Disadvantages	<ul> <li>Limited focal plane filling due to potential beam blockage</li> </ul>	<ul> <li>Susceptible to internal standing waves unless properly matched to incoming radiati</li> </ul>	<ul> <li>Beam diameter (f-number) varies with frequency (lens diameter in wavelengths)</li> </ul>	<ul> <li>Low f/D results in rapid rise of coma lobe for large off-axial displacements</li> </ul>	257					
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PHS-SPTHZDFP.VGS-3/7/96











Fig. 4: A comparison of the calculated (dashed) and measured (solid) field strength along the surface of a dielectric-filled parabola with a thin dipole antenna at the focal point at 10 GHz. The calculations are based on data published in [19]. The measurements were made with a dipole probe placed in intimate contact with the lens surface. The excessive ripple is due to reflections off the air-dielectric interface.



## CALCULATED POWER PATTERN USING MEASURED FIELDS



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Dielectric-Filled Parabola: Array Properties (P.A. Stimson, R.J. Dengler, P.H. Siegel, H.G. LeDuc) • Modeled array beam properties of dipoles on quartz in 2X5 arrangement	<ul> <li>Off-axis beam placement and RF/IF filtering empirically determined through measurements</li> <li>Implemented first array at 200 GHz in submersion dewar at 4K with niobium SIS tunnel junctions from LeDuc and Bumble - 10 working elements - DSB noise performance between 75K and 250K with fixed LO</li> </ul>	<ul> <li>UMichigan developed first complete analysis of DFP structure with predictions of on/off-axis performance using slots and dipoles (Filipovic, Ali-Ahmad, Raman, Gautier, Rebeiz)</li> </ul>	<ul> <li>University of Koeln (Bischoff, Jacobs) developed slot array on silicon DFP structure and performed extensive analysis of off-axis beam patterns and impedance matching. Currently working on 490 GHz implementation.</li> </ul>	<ul> <li>Corrected array optics concept (Schmidt system) designed but never implemented <sup>pHS-SPTHZDEP.VGS-3//96</sup> </li> </ul>
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## Planar Array Receiver Exploded Schematic





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Seventh International Symposium on Space Terahertz Technology, Charlottesville, March 1996 Dielectric-Filled Parabola: Semiconductor Mixer
(P. Siegel, R. P. Smith)
<ul> <li>Combined twin-slot half-wave resonant slot antenna design of Kerr, Siegel, Mattauch with coplanar feed (similar to Gearhart and Rebeiz) and integrated GaAs T-anode Schottky diode (produced by R. Peter Smith and S. Martin) on silicon DFP to produce 2.5 THz planar semiconductor mixer concept</li> </ul>
• Mixer prototyped at 200 GHz - Measured noise temperature $\approx\!1000K$ DSB with 25 \Omega planar diode (performance improvement of 2X expected with better device)
<ul> <li>Currently fabricating 2.5 THz version</li> </ul>





200 GHz Open Structure Mixer Prototype: Twin-Slot Antennas on a Silicon DFP





Twin-Slot Antenna on a Silicon DFP: RF Beam Patterns Measurement Frequency: 190GHz

**Twin-Slot Antenna on a Silicon DFP: Mixer Performance** Diode Parameters: Rs=25 Ohms, Is=8e-15 A, n=1.23, phi=.9V, Cj0 (nominal)=3.5 fFd



Dielectric-Filled Parabola: Planar Multiplier         (M. Kim, R.P. Smith, S. Martin, P. H. Siegel)         • Combined quad-bridge-diode multiplier concept and two-pair of cross polarized half-wave resonant slot antennas to produce all-planar doubler and quadrupler circuits for submillimeter wavelengths	<ul> <li>Novel coplanar-waveguide matching circuitry designed to efficiently couple diodes to</li></ul>	<ul> <li>Two-component matching layer (Corning 9606 ceramic &amp; polyethylene sheet)</li></ul>	<ul> <li>Complete HFSS/Momentum analysis yields beam properties, optimum input/output</li></ul>	<ul> <li>20 GHz model using quartz parabola with commercial quad-mixer diode package</li></ul>	<ul> <li>Currently fabricating 160-320 GHz and 320-640 GHz doubler and 160-640 GHz</li></ul>
	input/output antennae	combined to yield simultaneous beam match to silicon DFP at input and output	impedances	gives performance within predicted limits	quadrupler on silicon DFP



600/1200 GHz doubler performance prediction

Input power level	Diode size	Expected output power
500 uW	0.5um x 1um	100 uW
2 mW	0.5um x 3um	600 uW

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