

# A 900GHz Broadband Balanced Frequency Quadrupler

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Terahertz sources have attracted recent interest for both high resolution radar imaging and radio astronomy applications. For such applications, it is important to have compact sources that produce enough power to pump the front end of the transceiver.

For terahertz multiplier sources, cascading multipliers is a pragmatic approach. It typically consists of a chain of doublers and triplers[1][2] which are selected to yield the desired output frequency. The final efficiency of a multi-stage chain, as a result, is often on the order of a few percent or less [3]. Moreover, inter-stage mismatches in the chain can readily influence adjacent stages by pulling them from their optimum operating status and reducing efficiencies. Direct multiplication to a high-order harmonic greater than the third is desirable for higher frequency band, even with the challenges including proper termination of all intermediate harmonics (idlers).

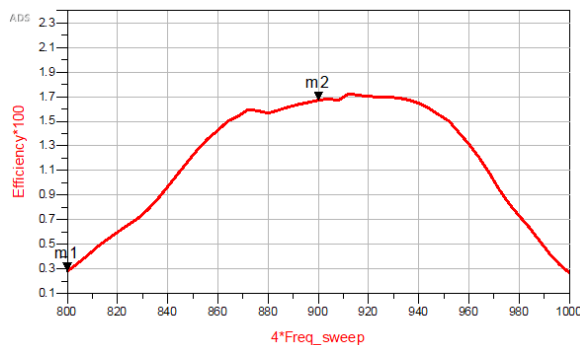


Fig.1. Power efficiency simulation of the 900 GHz GaAs membrane frequency Quadrupler

The work presented here applies the GaAs membrane substrate technology to implement an integrated frequency quadrupler. This Quadruplers features 2 anodes in series balanced configuration and is connected to a split waveguide-block by metallic beam-leads. The simulation results gives about 0.8%-1.7% multiplying efficiency from 830 GHz to 980 GHz and 0.4 mW of maximum output power with 24 mW of input power at 225 GHz.

## REFERENCES

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