Superconducting Local Oscillators; Development and Optimization.

P.N. Dmitriev¹, L.V. Filippenko¹, K.V. Kalashnikov¹,², M.E. Paramonov¹, K.I. Rudakov¹,², and V.P. Koshelets¹, *¹
Kotel’nikov Institute of Radio Engineering and Electronics, Moscow, 125009, Russia
²Moscow Institute for Physics and Technology (State University), Dolgoprudny, Russia
*Contact: valery@hitech.cplire.ru, phone +7-495-6293418

Abstract— Different types of the superconductor local oscillators were considered for integration with a SIS-mixer to build fully superconducting integrated receivers (SIR). The Josephson Flux Flow Oscillators (FFO) based on Nb-AlOx-Nb and Nb-AlN-NbN junctions have proven to be the most developed for such integration. The continuous frequency tuning of the FFO over the 250 - 750 GHz frequency range and the possibility of FFO phase stabilization have been achieved. The output power of the FFO is sufficient to pump integrated on the same chip SIS mixer in a wide frequency range; the FFO power can be electronically adjusted. The FFO free-running linewidth has been measured between 0.3 and 5 MHz; resulting in the spectral ratio of the phase-locked FFO from 99 to 70% over the whole frequency range. The possibility of reaching the phase noise of the order of -90 dBc at an offset from a carrier frequency of more than 100 kHz has been demonstrated experimentally. To improve further FFO parameters and to extend its frequency range a number of new FFO designs were developed and investigated. The goal is to simplify the FFO operation at lower frequencies (through Fiske steps suppression at frequencies below Josephson self-coupling boundary) as well as to extend the FFO operation frequency beyond 1 THz. In this report an overview of development of superconducting integrated THz local oscillators is presented.

INTRODUCTION

Josephson junctions have been considered as natural terahertz oscillators for more than half a century, ever since Josephson discovered the effects named after him [1], [2]. Since that time, many quite different types of Josephson oscillators have been proposed and studied [3] – [13], but only a few of them were developed at level suitable for real applications. Let us consider one of the most attractive applications - the direct integration of a Josephson Local Oscillator (JLO) with the most sensitive heterodyne SIS mixer. There are a number of important requirements of the JLO’s properties to make it suitable for application in the phase-locked Superconducting Integrated Receiver (SIR). The continuous frequency tuning of the JLO over a wide frequency range (usually more than 100 GHz) and a possibility of the JLO’s phase stabilization at any frequency in the operation range are required for most applications. The output power of the JLO should be sufficient to pump the matched SIS mixer within a wide frequency range and it can be electronically adjusted. Obviously, the JLO should emit enough power to pump an SIS mixer (of about 1 µW), taking into account a specially designed mismatch of about 5 – 7 dB between the JLO and the SIS mixer, which should be introduced to avoid leakage of the input signal to the LO path. It is a challenge to realize the ultimate performance of the separate superconducting elements after their integration into a single-chip device. Another very important issue is the linewidth of the JLO. Even for wideband room-temperature PLL systems, the effective regulation bandwidth is limited by the length of the cables in the loop (about 10 MHz for a typical loop length of two meters). This means that the free-running JLO linewidth has to be well below 10 MHz to ensure stable JLO phase locking with a reasonably good spectral ratio (SR) — the ratio between the carrier and the total power emitted by the JLO.

NB-BASED FLUX-FLOW OSCILLATORS

The Josephson Flux Flow Oscillators (FFO) [14] – [19] based on Nb-AlOx-Nb and Nb-AlN-NbN junctions have proven [20] – [25] to be the most developed superconducting local oscillator for integration with an SIS mixer in a single-chip submm-wave Superconducting Integrated Receiver [26] – [29]. The FFO is a long Josephson tunnel junction of the overlapped geometry in which an applied DC magnetic field and a DC bias current, \( I_b \), drive a unidirectional flow of fluxons, each containing one magnetic flux quantum, \( \Phi_0 = h/2e \approx 2*10^{-15} \text{ Wb} \). Symbol \( h \) represents Planck’s constant and \( e \) is the elementary charge. An integrated control line with the current \( I_{cl,s} \) is used to generate the DC magnetic field that is applied to the FFO. According to the Josephson relation, the junction oscillates with a frequency \( f = \frac{1}{2\Phi_0} \times V \) (about 483.6 GHz/mV) if it is biased at voltage \( V \). The fluxons repel each other and form a chain that moves along the junction. The velocity and density of the fluxon chain, and thus the power and frequency of the submm-wave signal emitted from the exit end of the junction due to the collision with the boundary, may be adjusted independently by the appropriate settings of \( I_b \) and \( I_{cl} \). The FFO differs from the other members of the Josephson oscillator family by the need for these two control currents, which in turn provides the possibility of an independent frequency and power tuning.
We experimentally investigated a large number of the FFO designs. The length, $L$, and the width, $W$, of the FFO used in our study were $300–400 \mu m$ and $4–28 \mu m$, respectively. The value of the critical current density, $J_c$, was in the range of $4–8 \text{kA/cm}^2$, giving a Josephson penetration depth of $\lambda_c \approx 6–4 \mu m$. The corresponding value of the specific resistance was $R_n^*L^*W \approx 50–25 \text{Ohm*}\mu m^2$. For the numerical calculations we used a typical value of the London penetration depth, $\lambda_L \approx 90 \text{nm}$ for all-Nb junctions, and a junction-specific capacitance $C_s \approx 0.08 \text{pF/}\mu m^2$. The active area of the FFO (i.e., the AlO$_x$ or the AlN tunnel barrier) is usually formed as a long window in the relatively thick (200–250 nm) SiO$_2$ insulation layer, sandwiched between the two superconducting films (the base and wiring electrodes). The so-called “idle” region consists of the thick SiO$_2$ layer adjacent to the junction (on both sides of the tunnel region) between the overlapping electrodes. It forms a transmission line parallel to the FFO. The width of the idle region ($W_1 = 2–14 \mu m$) is comparable to the junction width. The idle region must be taken into account when designing an FFO with the desired properties. In our design, it is practical to use the flat-bottomed electrode of the FFO as a control line in which the current $I_{c2}$ produces the magnetic field, which is mainly applied perpendicular to the long side of the junction.

Previously, the Nb-AlOx-Nb or Nb-AlN-NbN layers were successfully used for the FFO’s fabrication. Traditional all-Nb circuits are constantly being optimized but there seems to be a limit for linewidth optimizations at certain boundary frequencies due to the Josephson self-coupling (JSC) effect [21], as well as a high frequency limit, imposed by the Nb gap frequency (~700 GHz). This is the reason to develop novel types of junctions based on materials other than Nb. We reported on the development of the high-quality Nb-AlN-NbN junction-production technology [30]. The implementation of an AlN tunnel barrier in combination with an NbN top superconducting electrode provides a significant improvement in the quality of the SIS junction. The gap voltage of the junction $V_g = 3.7 \text{mV}$. From this value, and the gap voltage of the Nb film $\Delta_{\text{Nb}}/e = 1.4 \text{mV}$, we have estimated the gap voltage of our NbN film $\Delta_{\text{NbN}}/e = 2.3 \text{mV}$ [25]. The use of Nb for the top “wiring” layer is preferable due to smaller losses of Nb when compared to NbN below 720 GHz. Furthermore, the matching structures developed for the all-Nb SIRs can be used directly for the fabrication of receivers with Nb-AlN-NbN junctions. The general behavior of the new devices is similar to that of the all-Nb ones; even the control currents, necessary to provide magnetic bias for the FFO, were nearly the same for the FFOs of similar designs.

A family of the Nb-AlN-NbN FFO 1VCs, measured at different magnetic fields produced by the integrated control line, is presented in Fig. 1 ($L = 300 \mu m$, $W = 14 \mu m$, $W_1 = 10 \mu m$). A single SIS junction with an inductive tuning circuit was employed as a harmonic mixer (HM) for the linewidth measurements. The tuning and matching circuits were designed to provide “uniform” coupling in the frequency range of 400–700 GHz. Measured values of the HM current induced by the FFO oscillations (HM pumping) are shown in Fig. 1 by the color scale. The HM pumping for each FFO bias point was measured at a constant HM bias voltage of 3 mV (pumping is normalized on the current jump at the gap voltage, $I_g = 140 \mu A$). From Fig. 1 one can see that an FFO can provide a large enough power over the wide frequency range, which is limited at higher frequencies only by the Nb superconducting gap in transmission line electrodes (base and wiring layers) and below 400 GHz by the design of the matching circuits.

The feature at approximately 600 GHz where the curves get denser is a JSC (Josephson Self-Coupling) boundary voltage. It was initially observed for all-Nb FFOs [21]. The JSC effect is the absorption of the FFO-emitted radiation by the quasiparticles in the cavity of the long junction. It considerably modifies the FFO’s properties at the voltages $V \approx V_{\text{JSC}} = 1/3*V_g$ ($V_{\text{JSC}}$ corresponds to 620 GHz for the Nb-AlN-NbN FFO). Just above this voltage, the differential resistance increases considerably; that results in an FFO linewidth broadening just above this point. This, in turn, makes it difficult or impossible to phase-lock the FFO in that region. For an Nb-AlOx-Nb FFO, the transition corresponding to $V_{\text{JSC}} = V_g/3$ occurs around 450 GHz. Therefore, by using the Nb-AlN-NbN FFOs we can cover the frequency gap from 450 to 550 GHz that is imposed by the gap value of all-Nb junctions. The feature in Fig. 1 around 1 mV is very likely due to a singularity in the difference between the superconducting gaps $\Delta_{\text{NbN}} - \Delta_{\text{Nb}}$.

Continuous frequency tuning at frequencies below 600 GHz for the Nb-AlN-NbN FFOs of moderate length is possible, although the damping is not sufficient to completely suppress the Fiske resonant structure at frequencies below $V_g/3$. For short junctions with a small $\alpha$ (wave attenuation factor), the distance between the steps in this resonant regime can be as large, so that it is only possible to tune the FFO within a certain set of frequencies. For a 300–400 $\mu m$ long Nb-AlN-NbN junction, this is not the case — the quality factor of the resonator formed by a long Nb-AlN-NbN Josephson junction is not so high at frequencies $> 350$ GHz. Therefore, the resonance steps are slanting and the distance between them is
The FFO linewidth (LW) has been measured in a wide frequency range from 300 GHz up to 750 GHz by using a specially developed experimental technique [20] – [24]. A specially designed integrated circuit incorporates the FFO junction, the SIS harmonic mixer and the microwave matching circuits. Both junctions are fabricated from the same Nb/AlN/NbN junction, the SIS harmonic mixer and the microwave matching circuits. Both junctions are fabricated from the same Nb/AlN/NbN or Nb/AlOx/Nbtrilayer. A block diagram of the set-up for the linewidth measurements is described in [22].

Fig. 2. The IVCs of the SIS mixer: unpumped = solid curve; pumped at different frequencies = dashed and dotted lines (color online).

LINEWIDTH OF THE FFO AND ITS PHASE-LOCKING

The FFO linewidth (LW) has been measured in a wide frequency range from 300 GHz up to 750 GHz by using a specially developed experimental technique [20] – [24]. A specially designed integrated circuit incorporates the FFO junction, the SIS harmonic mixer and the microwave matching circuits. Both junctions are fabricated from the same Nb/AlN/NbN or Nb/AlOx/Nbtrilayer. A block diagram of the set-up for the linewidth measurements is described in [22].

Fig. 3. The IVCs of the SIS mixer: unpumped = black solid curve; pumped at different FFO bias currents (different powers) = lines with symbols; FFO frequency = 500 GHz (color online).

In order to accurately measure the FFO line shape, the IF signal must be time-averaged by the spectrum analyzer. To reduce low-frequency drift and interference from the bias supplies, temperature drift, etc., we used a narrow band (< 10 kHz) Frequency Discriminator (FD) system with a relatively low loop gain for the frequency locking of the FFO. With the FD narrow-band feedback system that stabilizes the mean frequency of the FFO (but which does not affect FFO’s line shape), we can accurately measure the free-running FFO linewidth, which is determined by the much faster internal (“natural”) fluctuations (see Fig. 4). The measured data are symmetrized relative to the center’s frequency; these data are shown by diamonds. The profile of the FFO line recorded when biased at the steep Fiske step (FS), where the differential resistance is extremely small, can be different from the one measured on the smooth Flux Flow step. Theoretically [58], the shape is Lorentzian for wide-band fluctuations, while for narrow-band interference, at frequencies smaller than the 17–20 GHz reference signal from a stable synthesizer. The required power level depends on the parameters of the HM; it is about of 1 µW for a typical junction area of 1 µm². The intermediate frequency (IF) mixer product (fIF ± fFFO – n·fSYN) at ~ 400 MHz is first boosted by a cooled HEMT amplifier (T< 5 K, gain = 30 dB) and then by a high-gain room-temperature amplifier.

The FFO signal is fed to the harmonic mixer (a SIS mixer operated in Josephson or quasiparticle mode) together with a 17–20 GHz reference signal from a stable synthesizer. The required power level depends on the parameters of the HM; it is about of 1 µW for a typical junction area of 1 µm². The intermediate frequency (IF) mixer product (fIF ± fFFO – n·fSYN) at ~ 400 MHz is first boosted by a cooled HEMT amplifier (T< 5 K, gain = 30 dB) and then by a high-gain room-temperature amplifier.
The FFO spectrum measured when biased on the Fiske step (\( V_{FFO} = 893 \ \mu V \), \( R_d = 0.0033 \ \Omega \), \( R_{cL} = 0.00422 \ \Omega \), \( \delta f_{AUT} = 1.2 \ \text{MHz} \)) is represented by the dash-dotted line. The symmetrized experimental data are shown by diamonds. The fitted theoretical Lorentzian and Gaussian profiles are shown by solid and dotted lines, respectively. The inset shows a close-up view of the central peak with the frequency axis multiplied five times [80].

The resulting IF signal is also supplied to the Phase-Locking Loop (PLL) system. The phase-difference signal of the PLL is fed to the FFO control-line current [15, 16, 75, 78–81]. Wideband operation of the PLL (10–15 MHz full width) is obtained by minimizing the cable loop’s length. A part of the IF signal is delivered to the spectrum analyzer via a power splitter (see Fig. 5, 6). All instruments are synchronized to the harmonics of a common 10 MHz reference oscillator. Dependencies of the free-running FFO linewidth and the Spectral Ratio (SR) for the phase-locked FFO on frequency for two different FFO technologies (Nb-Ox-Nb and Nb-AlN-NbN) are presented in Fig. 7. One can see that SR > 70% can be realized for Nb-AlN-NbN FFO in the range of 250–750 GHz.

**CRYOGENIC PHASE DETECTOR**

The local oscillator for the superconducting integrated receiver based on Flux flow oscillator has a tuning range of 250–750 GHz; in this range a free-run linewidth at half-height (~3 dB in power) may vary from hundreds kHz to several dozen of MHz (in some specific cases). When a conventional room-temperature (RT) PLL is used for the FFO phase locking, the FFO signal firstly down-converted at the harmonic mixer (HM) from hundreds of GHz to 400 MHz, and then the low frequency signal is amplified and sent from the cryostat to the semiconductor RT PLL. The phase of intermediate frequency signal is compared with the reference at the RT PLL, and the error signal is sent back to the cryostat, where it adjusts the FFO frequency. Long connecting cables are used to reduce the heat flow into the cryostat; however, it leads to a delay of the feedback signal of ~10 ns. The electronic block of the PLL adds another 7 ns, resulting in a limitation of about 15 MHz for the synchronization band of the RT PLL for FFO.

For efficient locking of wide Lorentzian lines emitted by FFO a PLL system with a very wide regulation bandwidth is required (due to slow decrease of the noise level with offset...
from the carrier). To overcome the limitations of the traditional RT PLL, we have developed the cryogenic high-harmonic phase detector (CHPD) [33], [34]. Implementation of the SIS junction both for down-conversion of oscillator frequency and generation of feedback signal to control the FFO frequency allows us to place all PLL elements in close vicinity to the oscillator. In turn, this provides significant reduction of loop time delay (less than 4 ns) and extremely large regulation bandwidth (up to 70 MHz). Since cryogenic PLL system consists of only superconductive and low-consumption elements, it could be integrated on the single chip with locked oscillator. As it is shown in Fig. 8, the CHPD PLL system could efficiently synchronize highly broad emission lines[29], [34].

Fig. 8. Experimentally measured FFO emission spectra downconverted from 634 GHz. The dashed and dotted line represents the autonomous FFO oscillation spectrum (line width, 16.8 MHz); the thin line represents the FFO spectrum synchronized using a room-temperature PLL (spectrum ratio 6%). The bold line shows the FFO spectrum synchronized by CHPD (spectrum ratio 84%).

SUPPRESSION OF THE FISKE STEPS

As it was already mentioned, continuous tuning of the FFO frequency below Vg/3 is limited due to presence of the Fiske steps (see Fig. 1). To overcome this limitation and considerably supress the Fiske resonances we have developed new FFO design with additional resistive elements (Fig. 9). It makes possible to increase damping and completely suppress the reflected electromagnetic waves and the Fiske resonant structure at frequencies just below Vg/3, see Fig. 10 and 11.

Fig. 9. Two layouts of the FFO with additional resistive elements (shown by green color) for suppression of the Fiske resonant structure at frequencies below Vg/3.

Fig. 10. IVCs of the Nb-AlN-NbN FFO with additional resistive elements for suppression of the Fiske resonant structure (design shown in Fig. 9 a); see Fig. 1 for comparison and detailed information.

Fig. 11. The differential resistance of the FFO IVCs measured at FFO current of about 25 mA. Data for FFO without suppression (see Fig.1) – blue curve; data for designs a) and b) are shown by red and green curves correspondingly.

CONCLUSIONS

The Flux Flow Oscillators (FFO) based on Nb-AlOx-Nb and Nb-AlN-NbN junctions provide unique combination of parameters unreachable for any competing technique:
- 250 – 750 GHz tuning range (in practice might be limited by the SIS matching circuitry);
- FFO frequency and power can be electronically adjusted; furthermore FFO can be phase-locked at any frequency providing spectral ratio > 70 % with phase noise of the order of -90 dBc;
- FFO was integrated with an SIS mixer in a single-chip sub-THz Superconducting Integrated Receiver (SIR) for the atmospheric-research instrument TErahertz and submillimeter Limb Sounder (TELIS).

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REFERENCES


